

		Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

 O.I.P.E. <i>SCANNED</i> <i>JKI</i> <i>Q.A.</i> <i>Su</i>	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/944171	F	438 257	546	2812 2815	Fenty

APPENDIX

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E

ESD protection circuit with very low input capacitance for high-frequency T/H0 ports

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
	(Primary Examiner)	(Date)	(Legal Instruments Examiner)	(Date)	ISSUE BATCH NUMBER

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